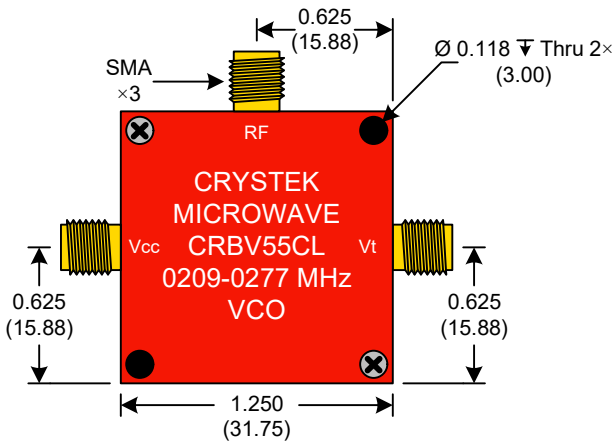


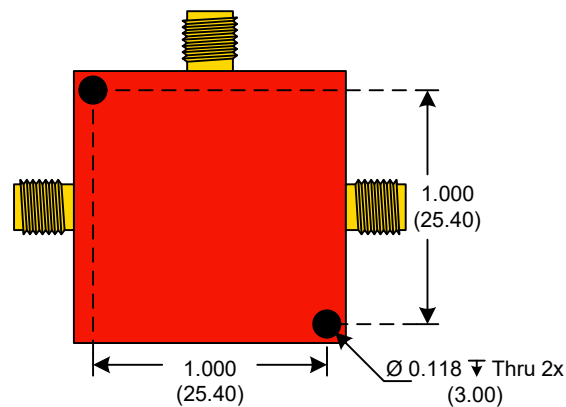


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			209	MHz
Upper Frequency:	277			MHz
Tuning Voltage:	0.5		6.0	VDC
Supply Voltage:	6.65	7.0	7.35	VDC
Output Power:	+2.0	+6.0	+10.0	dBm
Supply Current:		15	20	mA
Harmonic Suppression (2 nd Harmonic)		-15	-10	dBc
Pushing:		1.5	3.0	MHz/V
Pulling, all Phases:		2.5	5.0	MHz pk-pk
Tuning Sensitivity:		20		MHz/V
Phase Noise @ 10kHz offset:		-112	-106	dBc/Hz
Phase Noise @ 100kHz offset:		-132	-126	dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			220	pF
Operating Temperature Range:	-40		+70	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

FRONT



BACK



SIDE



● Unless otherwise specified, Dimensions are in: $\frac{\text{IN}}{\text{(mm)}}$



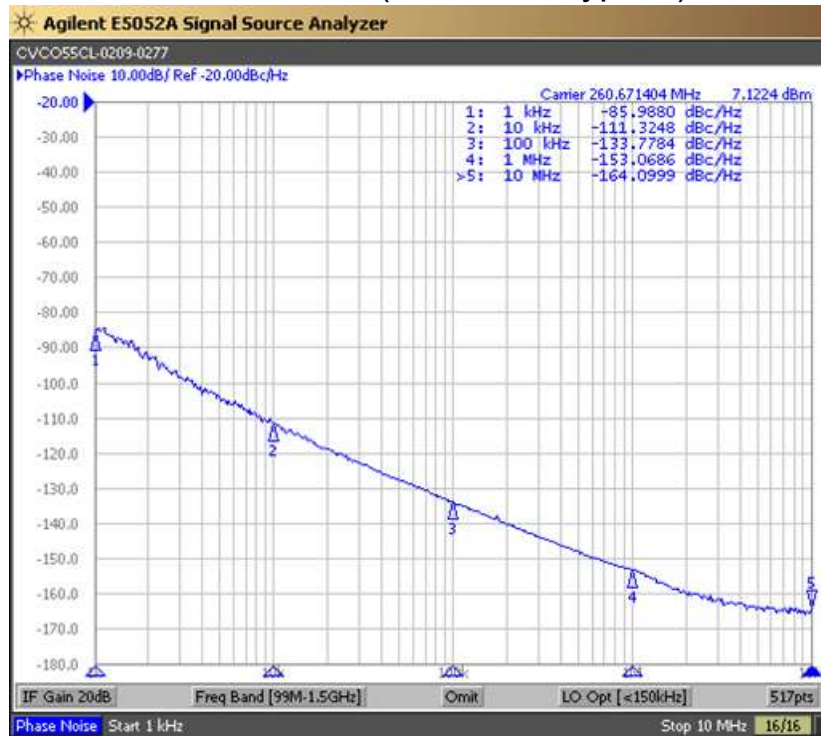
Proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

Product Control:

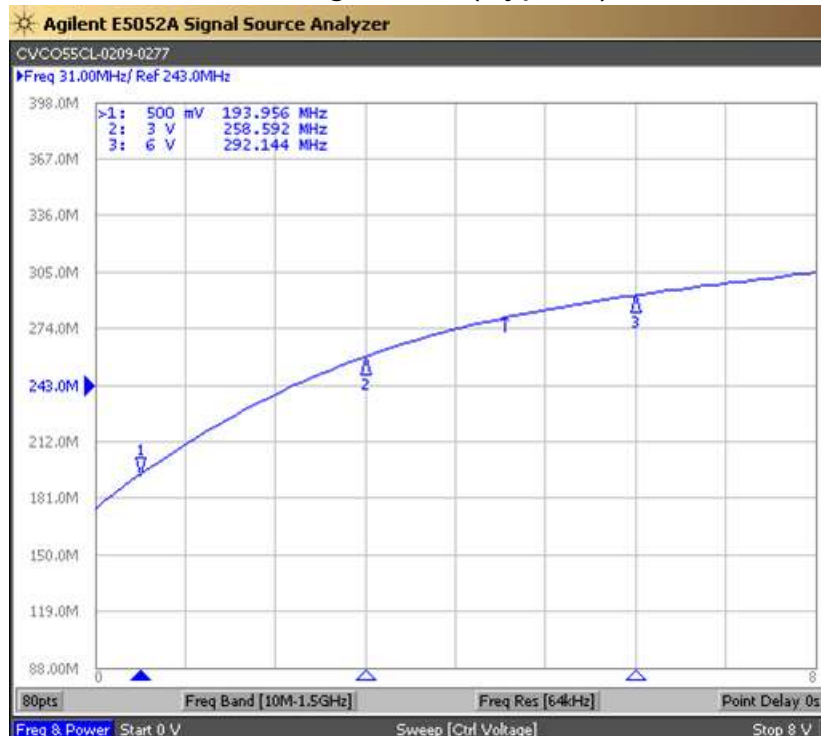
Crystek Part Number:	CRBV55CL-0209-0277	Release Date:	19-Mar-2026
Revision Level:	E	Responsible:	C. Vales



Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



Product Control:

Crystek Part Number:	CRBV55CL-0209-0277	Release Date:	19-Mar-2026
Revision Level:	E	Responsible:	C. Vales

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