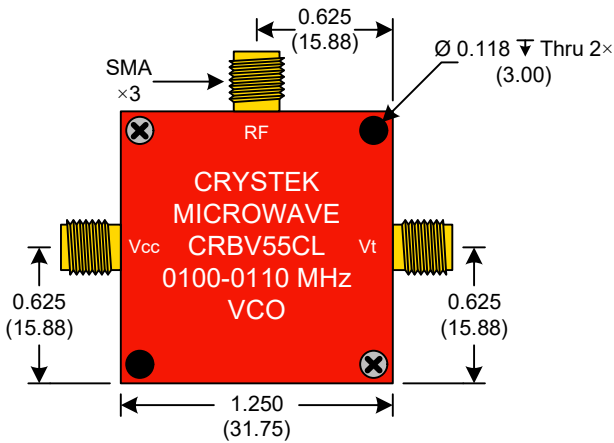




PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			100	MHz
Upper Frequency:	110			MHz
Tuning Voltage:	0.5		4.5	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	+5.0	+8.0	+11.0	dBm
Supply Current:		15	25	mA
Harmonic Suppression (2 nd Harmonic)		-10		dBc
Pushing:		0.2	0.4	MHz/V
Pulling, all Phases:		0.2	0.4	MHz pk-pk
Tuning Sensitivity:		8		MHz/V
Phase Noise @ 10kHz offset:		-119	-117	dBc/Hz
Phase Noise @ 100kHz offset:		-138	-133	dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			150	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

FRONT



BACK



SIDE



● Unless otherwise specified, Dimensions are in: $\frac{\text{IN}}{(\text{mm})}$

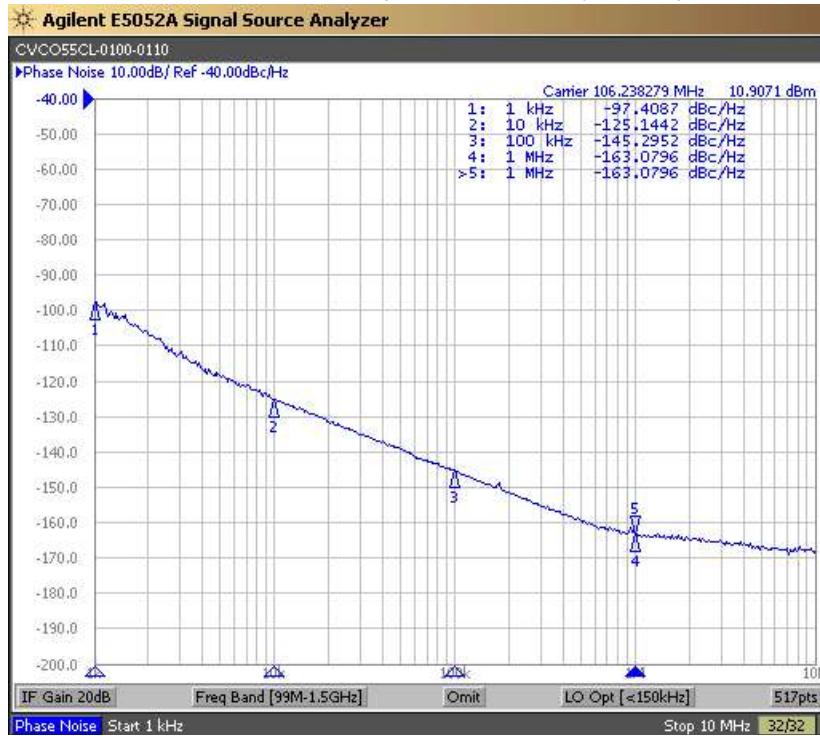


Proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

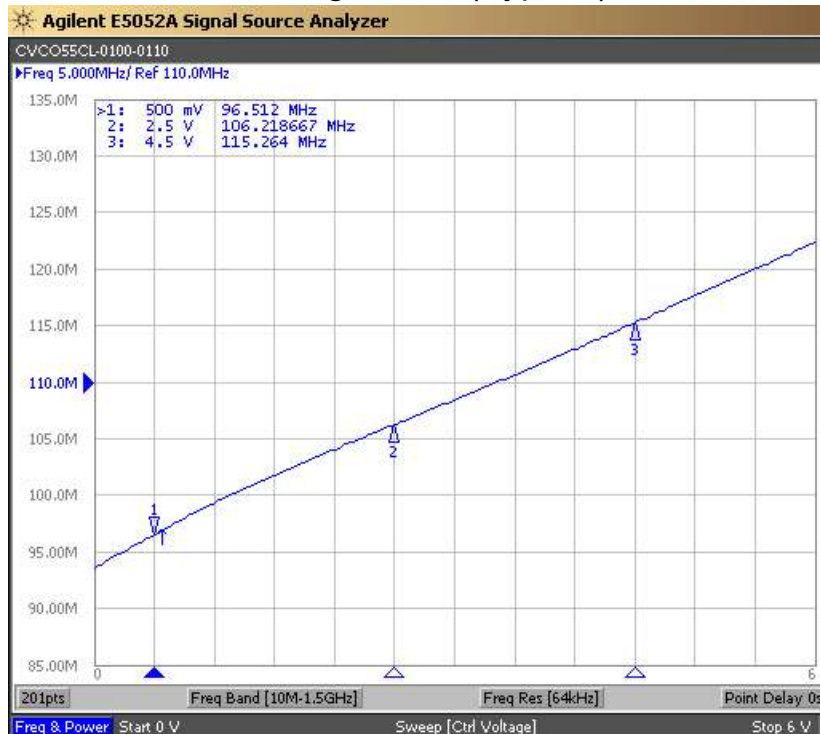
Product Control:

Crystek Part Number:	CRBV55CL-0100-0110	Release Date:	18-Mar-2026
Revision Level:	C	Responsible:	C. Vales

Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



Product Control:

Crystek Part Number:	CRBV55CL-0100-0110	Release Date:	18-Mar-2026
Revision Level:	C	Responsible:	C. Vales

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