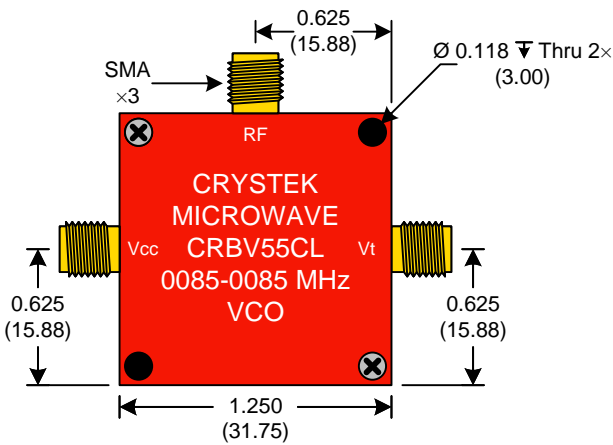
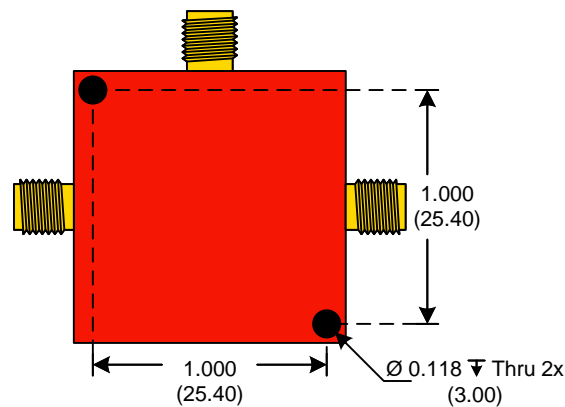


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			85	MHz
Upper Frequency:	85			MHz
Tuning Voltage:	1.0		4.0	VDC
Supply Voltage:	7.6	8.0	8.4	VDC
Output Power:	+6.0	+9.0	+12.0	dBm
Supply Current:		8	15	mA
Harmonic Suppression (2 nd Harmonic)		-20		dBc
Pushing:			5.0	MHz/V
Pulling, all Phases:			5.0	MHz pk-pk
Tuning Sensitivity:	2	3	4	MHz/V
Phase Noise @ 10kHz offset:		-120		dBc/Hz
Phase Noise @ 100kHz offset:		-137		dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			330	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

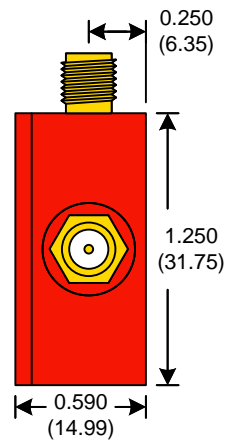
FRONT



BACK



SIDE



● Unless otherwise specified, Dimensions are in: $\frac{\text{IN}}{\text{mm}}$

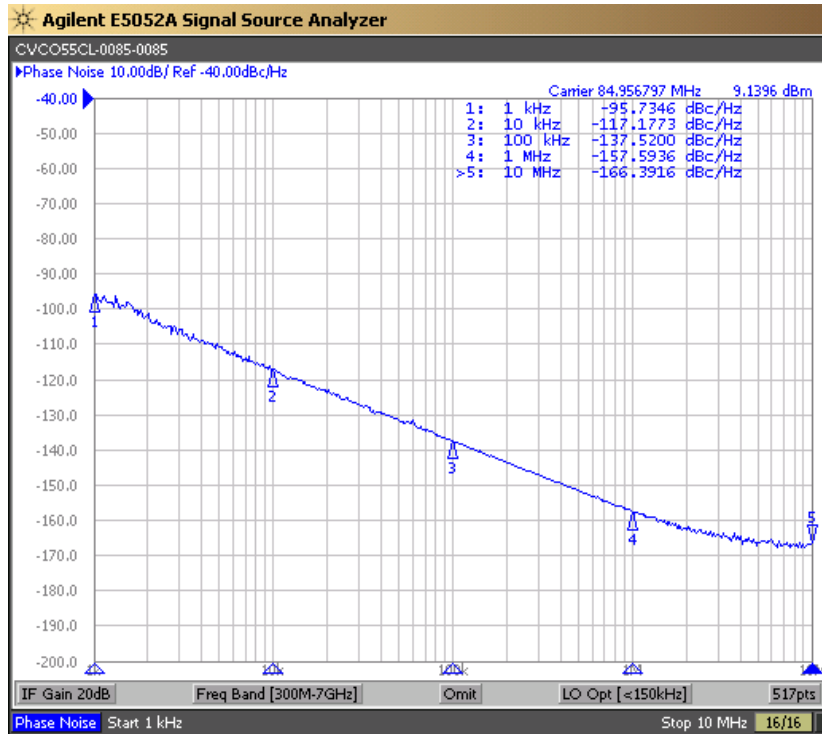


Proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

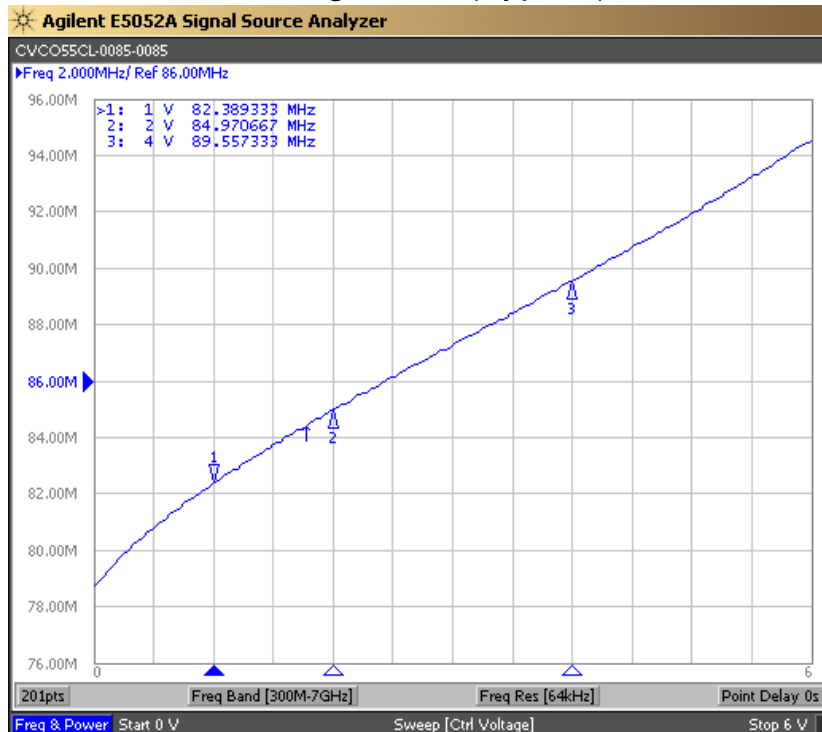
Product Control:

Crystek Part Number:	CRBV55CL-0085-0085	Release Date:	02-Oct-2024
Revision Level:	C	Responsible:	C. Vales

Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



Product Control:

Crystek Part Number:	CRBV55CL-0085-0085	Release Date:	02-Oct-2024
Revision Level:	C	Responsible:	C. Vales

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