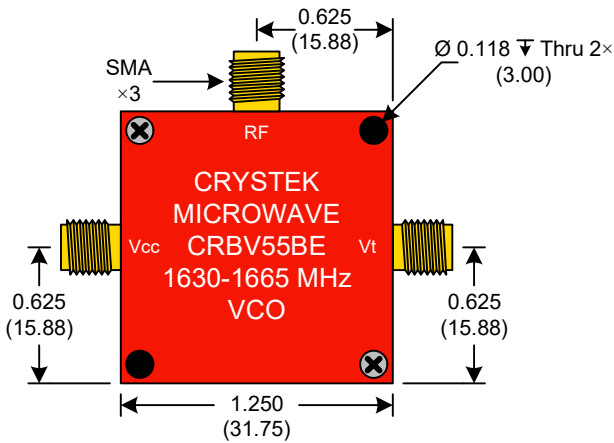


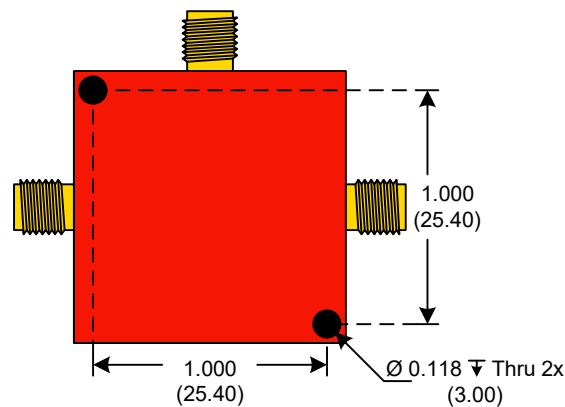


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			1630	MHz
Upper Frequency:	1665			MHz
Tuning Voltage:	0.5		4.5	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	+2.0	+4.0	+6.0	dBm
Supply Current:		15	25	mA
Harmonic Suppression (2 <sup>nd</sup> Harmonic)		-20	-8	dBc
Pushing:		1.0		MHz/V
Pulling, all Phases:		2.0		MHz pk-pk
Tuning Sensitivity:		18		MHz/V
Phase Noise @ 10kHz offset:		-102		dBc/Hz
Phase Noise @ 100kHz offset:		-124		dBc/Hz
Load Impedance:		50		$\Omega$
Input Capacitance:			50	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

FRONT



BACK



SIDE



● Unless otherwise specified, Dimensions are in:  $\frac{\text{IN}}{(\text{mm})}$



Proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

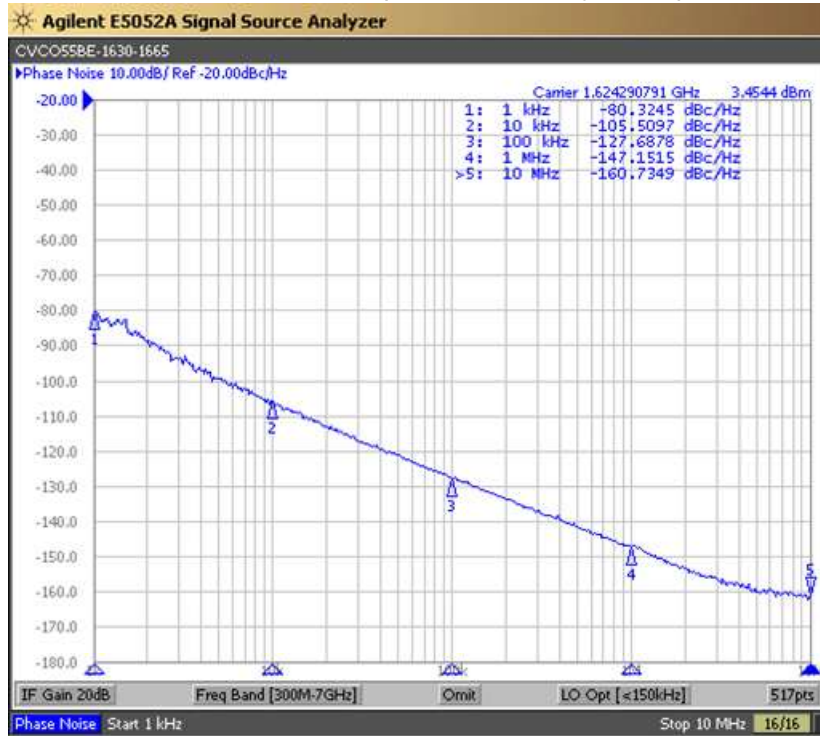
**Product Control:**

Crystek Part Number:	CRBV55BE-1630-1665	Release Date:	28-Jan-2026
Revision Level:	D	Responsible:	C. Vales

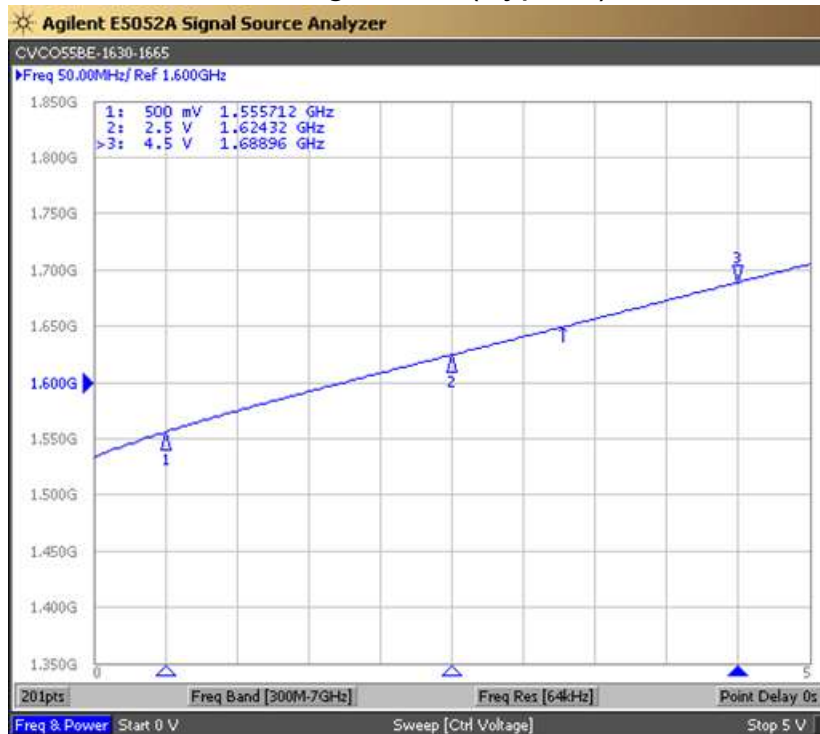




Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



**Product Control:**

Crystek Part Number:	CRBV55BE-1630-1665	Release Date:	28-Jan-2026
Revision Level:	D	Responsible:	C. Vales

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